

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

G. BENDAK et al.

) Group No.: Unknown

Serial No.: To be assigned

) Examiner: Unknown

Filed: Herewith

) Docket No. AMCC4860

For: **System and Method for Programming the Bit Error Rate of
Frame Synchronization Words in a Multidimensional
Digital Frame Structure**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

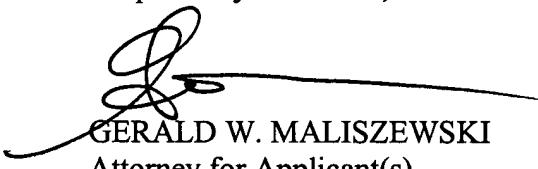
INFORMATION DISCLOSURE STATEMENT

Applicant hereby cites the documents listed in accompanying Form PTO-1449 with respect to the above-referenced patent application under the provisions of 37 CFR 1.97(b). Copies of the documents are attached.

The Examiner is respectfully requested to make the listed documents of record in connection with the prosecution of the subject application.

Respectfully submitted,

Date Dec. 22, 2000


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